

M.TECH II SEMESTER (R-25) REGULAR EXAMINATIONS , JULY 2026

SEMESTER END EXAMINATIONS TIME TABLE

Branch :CSE

DATE	SUBJECT CODE	SUBJECT NAME	TIME
27/07/2026	J225MT201 CS	Advanced Algorithms	10:00 AM TO 01:00 PM
29/07/2026	J225MT202 CS	Advanced Computer Architecture	10:00 AM TO 01:00 PM
31/07/2026	J225MT203 CS	Enterprise Cloud Concepts	10:00 AM TO 01:00 PM
03/08/2026	J225MT204 CS	Adhoc Sensor Networks	10:00 AM TO 01:00 PM

Branch :ES

DATE	SUBJECT CODE	SUBJECT NAME	TIME
27/07/2026	J225MT201 ES	Embedded Real Time Operating Systems	10:00 AM TO 01:00 PM
29/07/2026	J225MT202 ES	IoT System Design	10:00 AM TO 01:00 PM
31/07/2026	J225MT203 ES	Hardware Software Co-Design	10:00 AM TO 01:00 PM
03/08/2026	J225MT204 ES	Image and Video Processing	10:00 AM TO 01:00 PM

Branch :PEED

DATE	SUBJECT CODE	SUBJECT NAME	TIME
27/07/2026	J225MT201 PE	Advanced Power Converters Analysis	10:00 AM TO 01:00 PM
29/07/2026	J225MT202 PE	Electrical Drives	10:00 AM TO 01:00 PM
31/07/2026	J225MT203 PE	Integration and Control of Renewable Energy Systems	10:00 AM TO 01:00 PM
03/08/2026	J225MT204 PE	Power Quality Improvement Techniques	10:00 AM TO 01:00 PM


Controller of Examinations
Controller of Examination
Joginipally B.R. Engineering College
Bhaskar Nagar, Survey No:220, Yenkapally (V),
Himayath Nagar Post, Moinabad (M),
R.R.Dist, Hyderabad-500075.


Principal
Chief Superintendent / Principal
Joginipally B.R. Engineering College
Bhaskar Nagar, Survey No:220, Yenkapally (V),
Himayath Nagar Post, Moinabad (M),
R.R.Dist, Hyderabad-500075.

M.TECH II SEMESTER (R-23) SUPPLE EXAMINATIONS , JULY 2026

SEMESTER END EXAMINATIONS TIME TABLE

Branch :CSE

DATE	SUBJECT CODE	SUBJECT NAME	TIME
27/07/2026	J223MT201	Advanced Algorithms	10:00 AM TO 01:00 PM
29/07/2026	J223MT202	Advanced Computer Architecture	10:00 AM TO 01:00 PM
31/07/2026	J223MT203	Advanced Computer Networks	10:00 AM TO 01:00 PM
03/08/2026	J223MT204	Nature Inspired Computing	10:00 AM TO 01:00 PM

Branch :ES

DATE	SUBJECT CODE	SUBJECT NAME	TIME
27/07/2026	J223MT201-ES	ARM Microcontrollers	10:00 AM TO 01:00 PM
29/07/2026	J223MT202-ES	Digital Control Systems	10:00 AM TO 01:00 PM
31/07/2026	J223MT203-ES	IoT Architectures and System Design	10:00 AM TO 01:00 PM
03/08/2026	J223MT204-ES	Hardware and Software Co-Design	10:00 AM TO 01:00 PM

Branch :PEED

DATE	SUBJECT CODE	SUBJECT NAME	TIME
27/07/2026	J223MT201-PE	Advanced Power Electronic Converters-II	10:00 AM TO 01:00 PM
29/07/2026	J223MT202-PE	Electrical Drives	10:00 AM TO 01:00 PM
31/07/2026	J223MT203-PE	Power Electronics for Renewable Energy Systems	10:00 AM TO 01:00 PM
03/08/2026	J223MT204-PE	Power Quality Improvement Techniques	10:00 AM TO 01:00 PM

Controller of Examinations

Controller of Examination
Joginipally B.R. Engineering College
Bhaskar Nagar, Survey No:220, Yenkapally (V),
Himayath Nagar Post, Moinabad (M),
R.R.Dist, Hyderabad-500075.



Chief Superintendent / Principal
Joginipally B.R. Engineering College
Bhaskar Nagar, Survey No:220, Yenkapally (V),
Himayath Nagar Post, Moinabad (M),
R.R.Dist, Hyderabad-500075.